


# Brief CV

Name/中文姓名	Junlong Wang/王俊龙	Gender	Male 男	
Title (Pro./Dr.)	Senior Engineer	Country	China	
University/Department	The 13th Research Institute of China Electronics Technology Group Corporation			
Personal Web Sites	/			
Research Area	millimeter wave and terahertz technology(device,modal,circuits design and system application)			
<p>Brief introduction of your research experience:</p> <p>As the main developer, developed a series of millimeter-wave and terahertz Schottky diode chips, the cut-off frequency has reached 10THz,the chips has been successfully applied to domestic terahertz vector network analyzer and built device model, millimeter-wave and terahertz frequency multipliers and mixers has been realized based on self-developed devices .As the main inventor ,has applied for and authorized 51 patents in the field of millimeter-wave and terahertz, and 28 related academic papers have been published.</p>				

\*\*\*\*\*All the columns need to be filled in.